

Interfacing FlashRunner 2.0 with the STD_SPI driver

Driver Notes

Below the protocol supported:

- SPI [1kHz : 1MHz]
- MSB transmission

Driver Parameters

CPOL

SPI clock polarity: 0 or 1

CPHA

SPI clock phase: 0 or 1

NUM_BITS

Number of bits for each write/read.

Commands List

CONNECT

Power-on.

WRITE

WRITE <byte_0> <byte_1> <byte_2> ...

Write the bytes (or words) indicated, in the format 0xAABBCCDD. If NUM_BITS is 8 you can write 0xFF, if it's 16 0xEEFF and so on...

READOUT

READOUT <num_bytes>

Read the number of bytes indicated. Maximum 0x200.

WRITE_READ

WRITE_READ <byte_0> <byte_1> <byte_2> ...

Write the bytes (or words) indicated, in the format 0xFF. While writing it also reads the same number of bytes. If NUM_BITS is 8 you can write 0xFF, if it's 16 0xEEFF and so on...

DISCONNECT

Power-down.